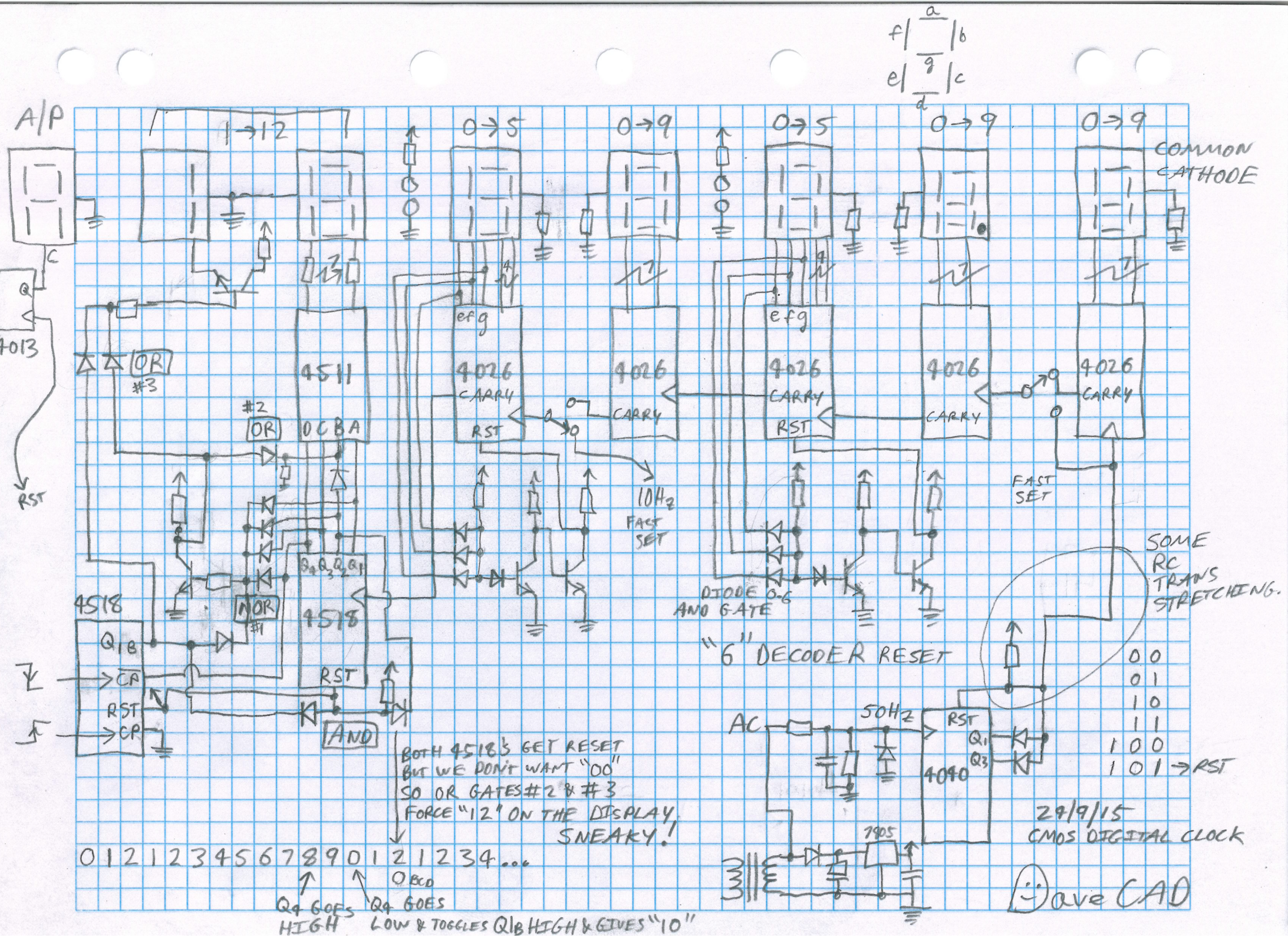
A/P
1→12
0→5
0→9
0→5
0→9
0→9
COMMON CATHODE
4013
RST
OR #3
#2 OR
4511
D C B A
efg
4026
CARRY
RST
10Hz FAST SET
FAST SET
4518
Q1B
CP
RST
CP
NOR #1
AND
DIODE 0-6 AND GATE
"6" DECODER RESET
SOME RC TRANS STRETCHING.
AC
50Hz
4040
7805
BOTH 4518'S GET RESET BUT WE DON'T WANT "00" SO OR GATES #2 & #3 FORCE "12" ON THE DISPLAY SNEAKY!
0 1 2 1 2 3 4 5 6 7 8 9 0 1 2 1 2 3 4 ...
BCD
Q4 GOES HIGH
Q4 GOES LOW & TOGGLES Q1B HIGH & GIVES "10"
0 0
0 1
1 0
1 1
1 0 0
1 0 1 → RST
24/9/15
CMOS DIGITAL CLOCK
Dave CAD