

Figure 48. Input Offset Voltage vs. Common-Mode Voltage

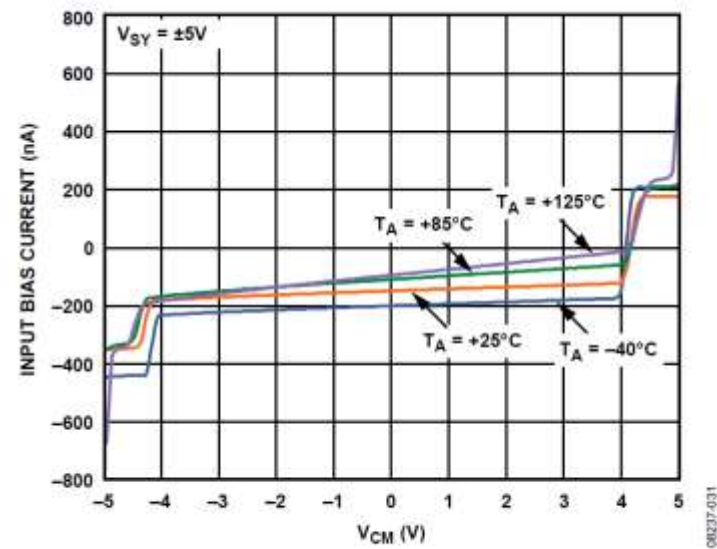


Figure 51. Input Bias Current vs.  $V_{CM}$  for Various Temperatures