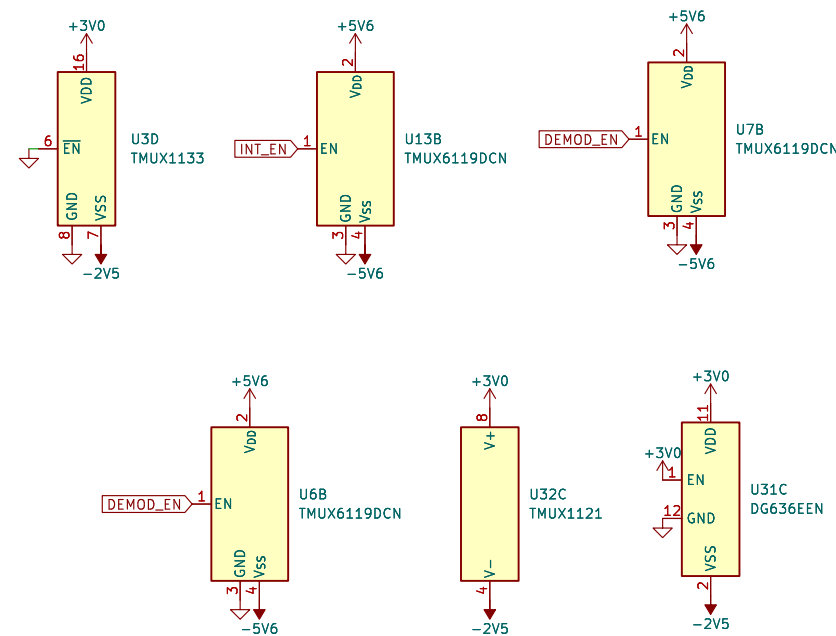
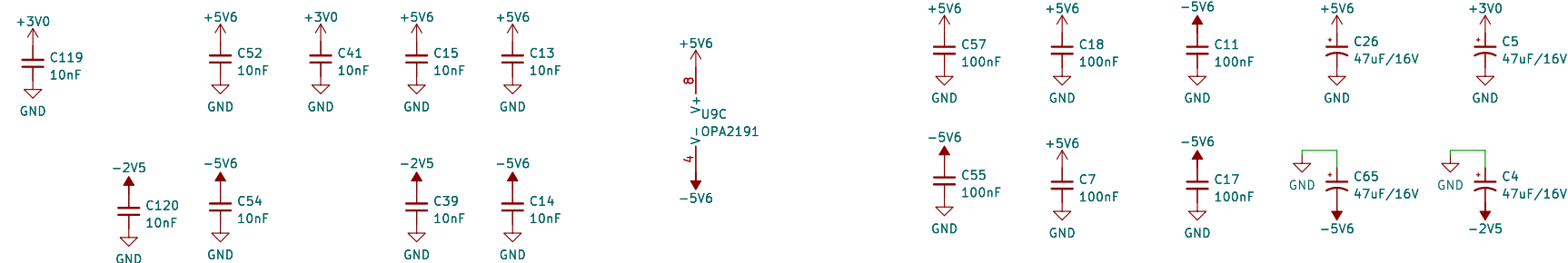
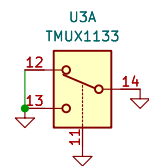
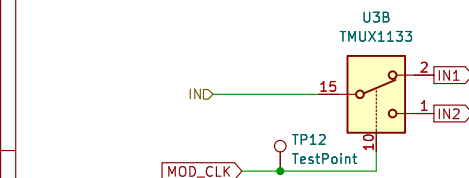
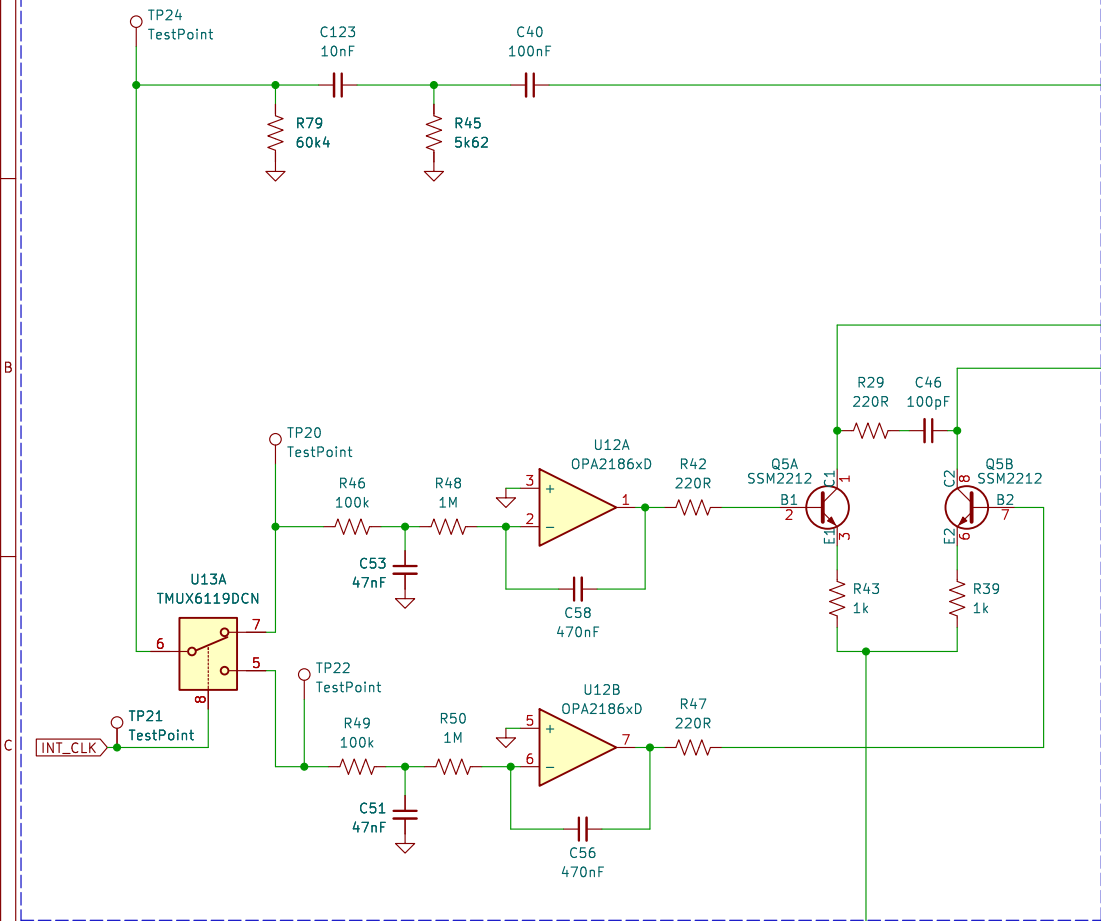
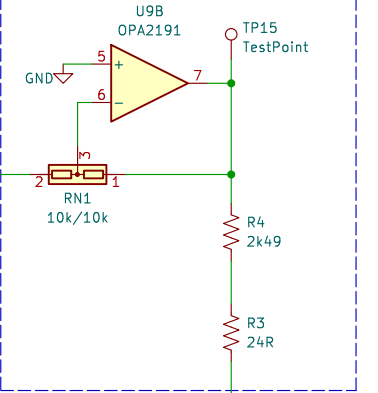


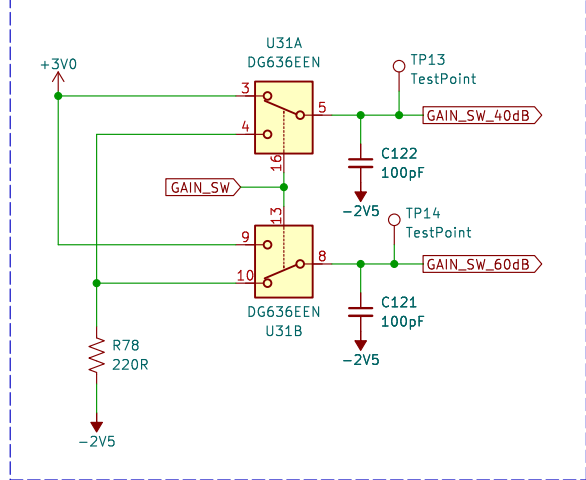
Vos Correction

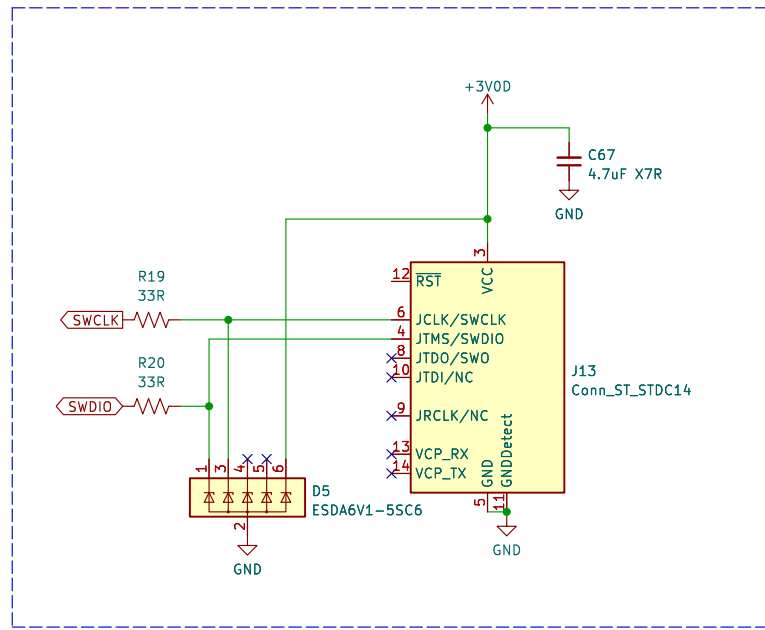


GND Current Cancellation



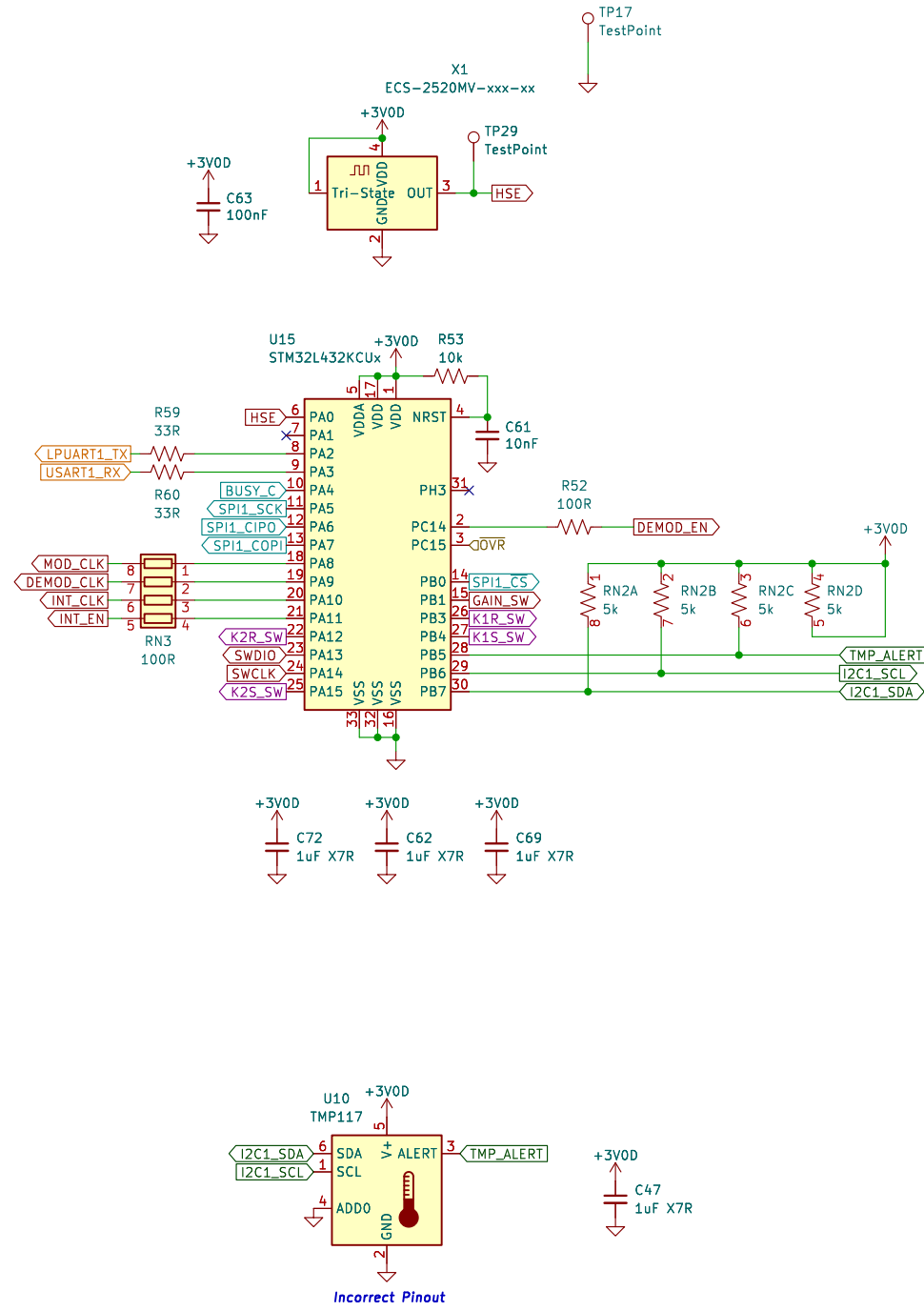
GAIN_SW Level Shift





The diagrams illustrate the SPI signal connections for four components: DG636EEN U16A, DG636EEN U16B, DG636EEN U17A, and DG636EEN U17B. Each component is shown with its pin numbers and the corresponding signal connections.

- DG636EEN U16A:** SCK (pin 5) is connected to SPI1_SCK (pin 4). A 3V0D supply is connected to pin 4, and a C75 1uF X7R capacitor is connected to pin 3. The output is labeled -2V5.
- DG636EEN U16B:** CS (pin 8) is connected to SPI1_CS (pin 9). A 3V0D supply is connected to pin 9, and a C78 1uF X7R capacitor is connected to pin 10. The output is labeled -2V5.
- DG636EEN U17A:** SDI (pin 5) is connected to SPI1_COP1 (pin 4). A 3V0D supply is connected to pin 4, and a C70 1uF X7R capacitor is connected to pin 3. The output is labeled -2V5.
- DG636EEN U17B:** SDO (pin 5) is connected to SPI1_CIP0 (pin 4). A 3V0D supply is connected to pin 4, and a C70 1uF X7R capacitor is connected to pin 3. The output is labeled -2V5.



The diagram illustrates a 4-bit parallel adder circuit using four 74113 decade counters (U1A, U1B, U1C, U1D). The counters are configured to add two 4-bit numbers (K1S, K1R) and produce a 4-bit sum (K2R, K2S). The circuit includes 3V00 and -2V5 power supplies, decoupling capacitors (C37, C35, C34, C36), and 74113 decade counters (U1A, U1B, U1C, U1D).

