Measurements SOLARTRON7060 „Loop around Integrator “

DC V-Input shorted ! **! ! all drawings not to be scaled ! !**

1. Diag 2.3 PCB 3 sheet 2 „Earthy Logic” (gnd. for the Oscilloscope: **TP18** next to IC**55**)
   TP4 (clock) 13,1072 MHz (if mains frequency is 50 Hz) **5 Vpp**

2. Diag 2.2 PCB 3 sheet 1 „Earthy Logic” (gnd. for the Oscilloscope: **TP18** next to IC**55**)

   IC 20 pin 1

   13,1072 MHz 5 Vpp
   (drawing should be sinusoidal ☺)

   IC 30 pin 13

   + 5 V 3,2 kHz
   0 V

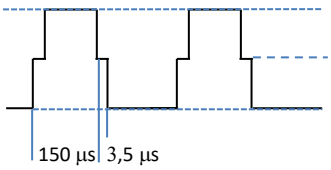
3. Diag 2.10 PCB 5 sheet 6 “floating” (gnd. for the Oscilloscope: **TP9** next to IC**58**)
   TP 805 **0 V**

   — 10 V 3,2 kHz
   — 15 V

4. Diag 2.8 ➔ into the loop
   TP 202

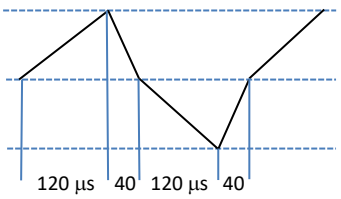
   0 V
   — 10V 3,2 kHz
   — 15 V

5. **TP 203**



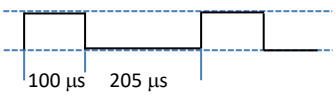
   + 8 V
   0 V
   — 8 V

6. **TP 204**



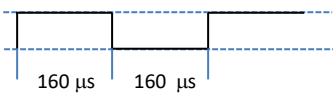
   + 5 V
   0 V
   — 5 V
   0 V

7. **TP 205**



   — 10 V
   — 15 V

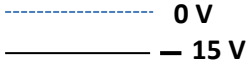
8. **TP 206** **0 V**



   — 10 V
   — 15 V

9. Diag 2.10

TP 804



10. Diag 2.8

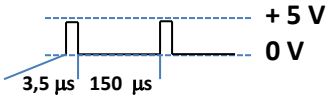
IC 204A Pin 3 and IC 204B Pin 11 → same as above

11. Diag 2.3 „Earthy Logic" (gnd. for the Oscilloscope: **TP18** next to IC**55**)

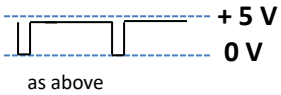
IC 24 Pin 2 and 12 → same as TP 205 and TP 206 on "floating"

Pin 3 and 11 → same as TP 4 (inverted)

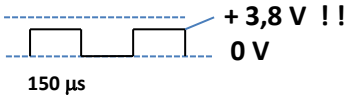
12. IC 30 Pin 1



IC 31 Pin 8

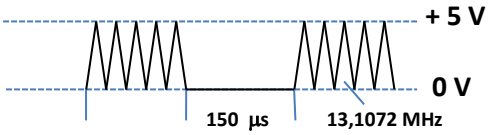


13. IC 24 Pin 5 + IC 31 Pin 9



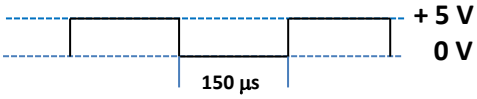
IC 24 Pin 6 + IC 30 Pin 2 → time same as above

14. TP 2 (count down) ← out of the loop now !



TP 3 (count up) same as above

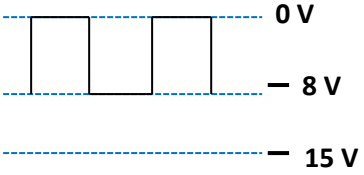
TP 5 (Latch)



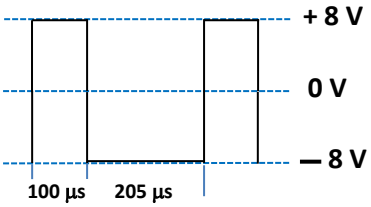
15. TP 6 (Count Reset)

<u>DC V-Input = + 10 V</u> , auto range, filter, 5 x 9

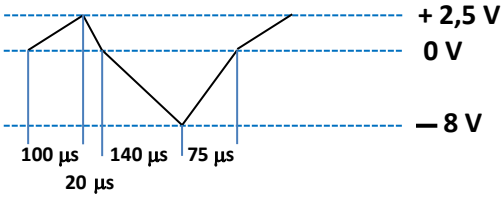
1. **TL1/TL2 (bridge) +10 V** (gnd. for the Oscilloscope: **TP9** next to IC**58**)
2. **TP 202**



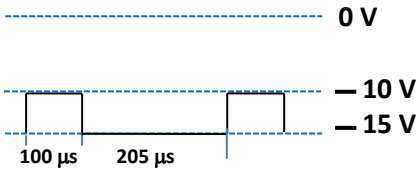
3. **TP 203**



4. **TP 204**



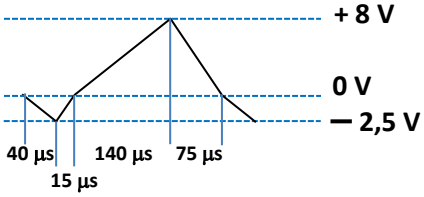
5. **TP 205 / TP 206**



6. **<u>DC V-Input = — 10 V</u> , auto range, filter, 5 x 9**

   TP 203 see above

   TP 204



   TP 205 / TP 206 see above