

IO	Name	F2	F4	F5	F6	R	S	Notes
0	GPIO0	CLK_OUT1	-	-	EMAC_TX_CLK	3	R	R (Reset state):
1	U0TXD	CLK_OUT3	-	-	EMAC_RXD2	3	-	0: input disabled
2	GPIO2	HSPIWP	HS2_DATA0	SD_DATA0	-	2	R	1: input enabled
3	U0RXD	CLK_OUT2	-	-	-	3	-	2: input enabled, PD
4	GPIO4	HSPIHD	HS2_DATA1	SD_DATA1	EMAC_TX_ER	2	R	3: input enabled, PU
5	GPIO5	VSPICS0	HS1_DATA6	-	EMAC_RX_CLK	3	-	
6	SD_CLK	SPICLK	HS1_CLK	U1CTS	-	3	-	S (Special notes):
7	SD_DATA_0	SPIQ	HS1_DATA0	U2RTS	-	3	-	R: RTC/analog
8	SD_DATA_1	SPID	HS1_DATA1	U2CTS	-	3	-	I: Input only
9	SD_DATA_2	SPIHD	HS1_DATA2	U1RXD	-	3	-	
10	SD_DATA_3	SPIWP	HS1_DATA3	U1TXD	-	3	-	Colors:
11	SD_CMD	SPICS0	HS1_CMD	U1RTS	-	3	-	Strapping
12	MTDI	HSPIQ	HS2_DATA2	SD_DATA2	EMAC_TXD3	2	R	R
13	MTCK	HSPID	HS2_DATA3	SD_DATA3	EMAC_RX_ER	2	R	Ethernet
14	MTMS	HSPICLK	HS2_CLK	SD_CLK	EMAC_TXD2	3	R	Input only
15	MTDO	HSPICS0	HS2_CMD	SD_CMD	EMAC_RXD3	3	R	SPI
16	GPIO16	-	HS1_DATA4	U2RXD	EMAC_CLK_OUT	1	-	
17	GPIO17	-	HS1_DATA5	U2TXD	EMAC_CLK_180	1	-	Strapping:
18	GPIO18	VSPICLK	HS1_DATA7	-	-	1	-	0: low - boot loader
19	GPIO19	VSPIQ	U0CTS	-	EMAC_TXD0	1	-	2: low when 0 low
21	GPIO21	VSPIHD	-	-	EMAC_TX_EN	1	-	12: high - boot fail
22	GPIO22	VSPIWP	U0RTS	-	EMAC_TXD1	1	-	15: low - no boot msg
23	GPIO23	VSPID	HS1_STROBE	-	-	1	-	
25	GPIO25	-	-	-	EMAC_RXD0	0	R	
26	GPIO26	-	-	-	EMAC_RXD1	0	R	
27	GPIO27	-	-	-	EMAC_RX_DV	0	R	
32	32K_XP	-	-	-	-	0	R	
33	32K_XN	-	-	-	-	0	R	
34	VDET_1	-	-	-	-	0	R,I	
35	VDET_2	-	-	-	-	0	R,I	
36	SENSOR_VP	-	-	-	-	0	R,I	
37	SENSOR_CAPP	-	-	-	-	0	R,I	
38	SENSOR_CAPN	-	-	-	-	0	R,I	
39	SENSOR_VN	-	-	-	-	0	R,I	