

Electronic DC Load - Analog Part

Variant: [No Variations]

12.02.2017
V111

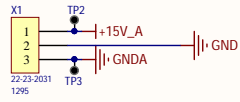
DRAFT

Page	Index	Page	Index	Page	Index	Page	Index
1	Cover Page	11		21		31	
2	Block Diagram	12		22		32	
3	Power Supply	13		23		33	
4	DUT Input	14		24		34	
5	Control Circuit Channel 1+2	15		25		35	
6	Control Circuit Channel 3+4	16		26		36	
7	ADC - DAC - VREF - IO Exp. - Temp. Sensor	17		27		37	
8		18		28		38	
9		19		29		39	
10		20		30		40	

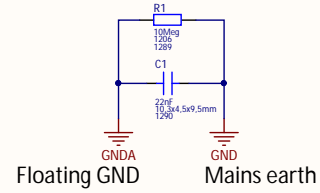


Power Supply

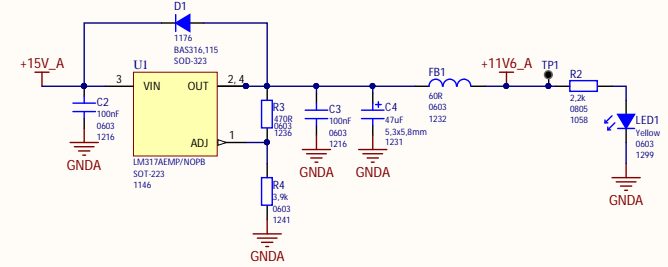
Power Supply Connector



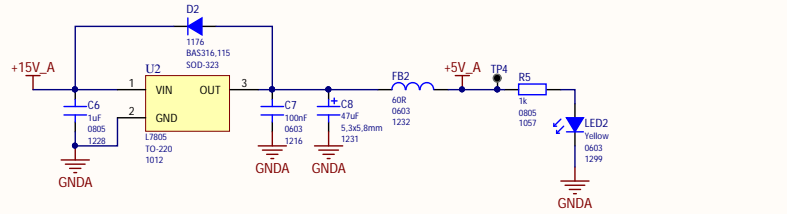
Floating GND and mains earth connection Place at DUT input



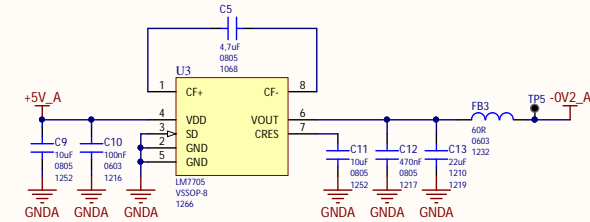
11V6 LDO (Opamp Supply)



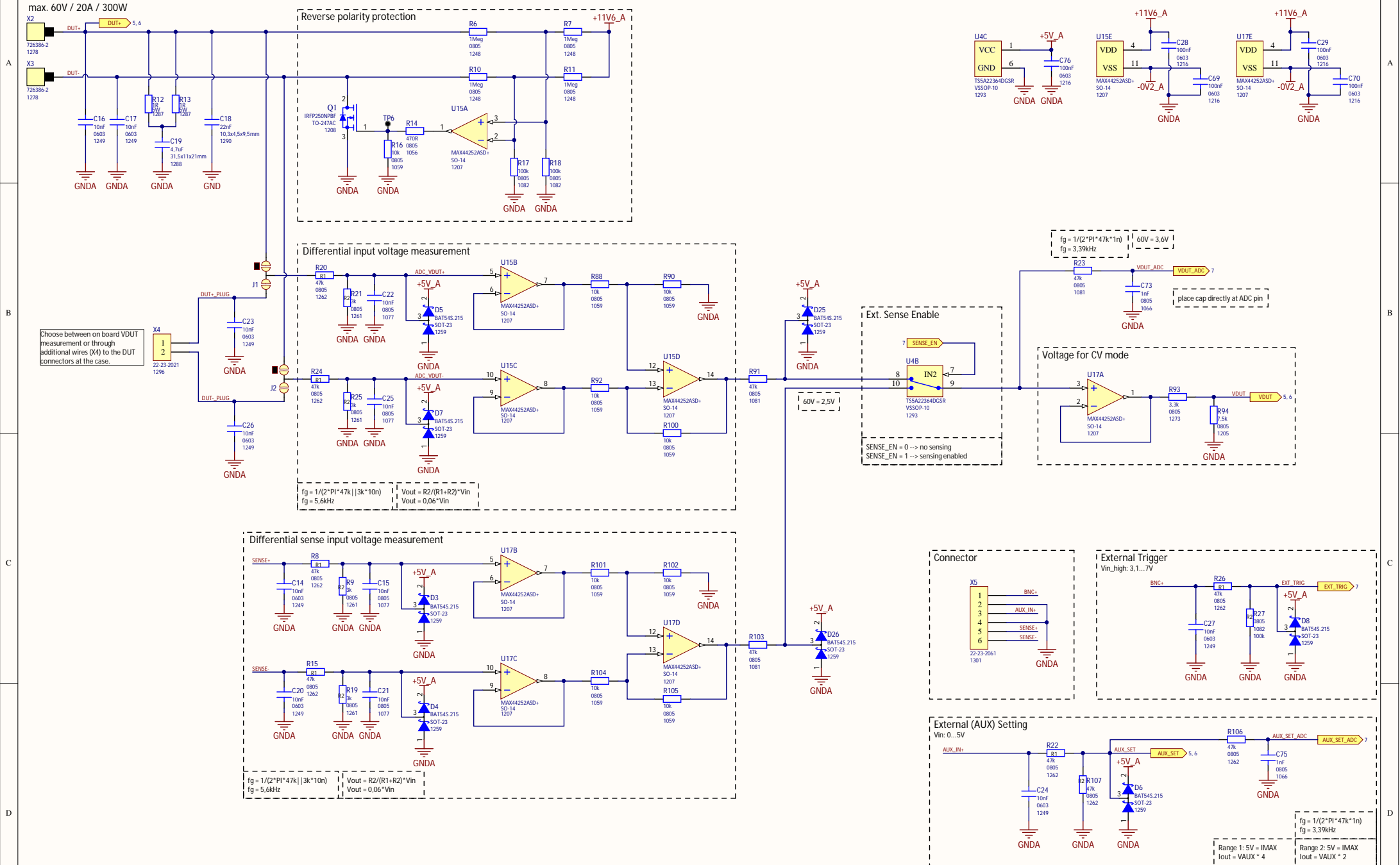
5V LDO (IC Supply)



-0V2 Charge Pump (Negative Opamp Supply)



DUT Input

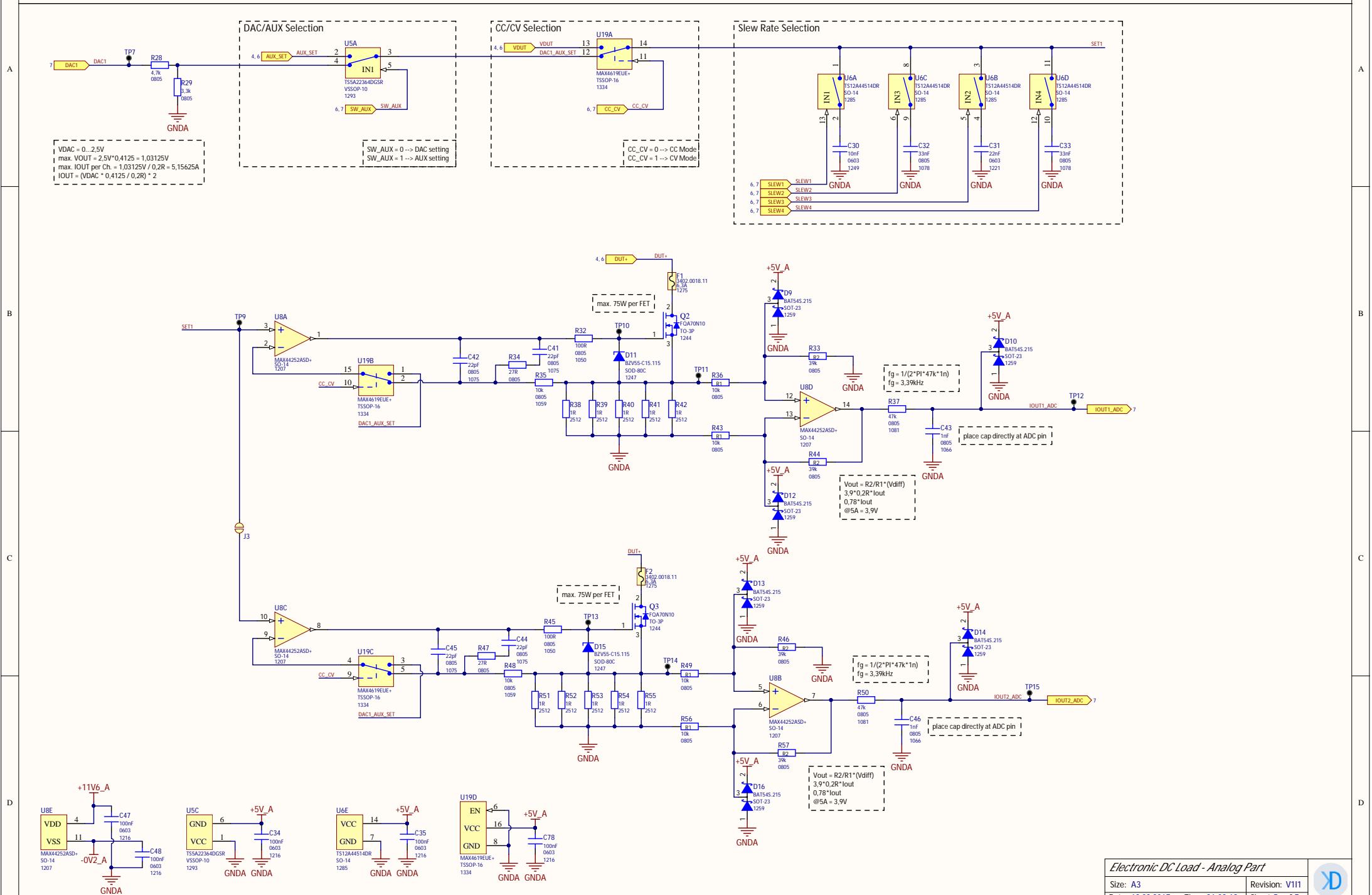


Electronic DC Load - Analog Part

Size: A3
Date: 12.02.2017
Time: 21:02:17
Revision: V111
Sheet 4 of 7



Current Control 1+2



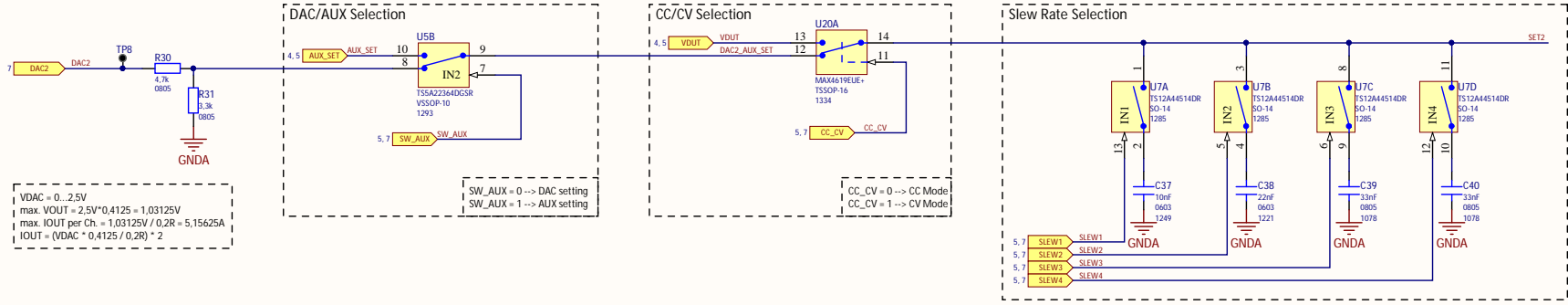
Electronic DC Load - Analog Part

Size: A3
 Date: 12.02.2017
 Time: 21:02:18
 Revision: V111
 Sheet 5 of 7

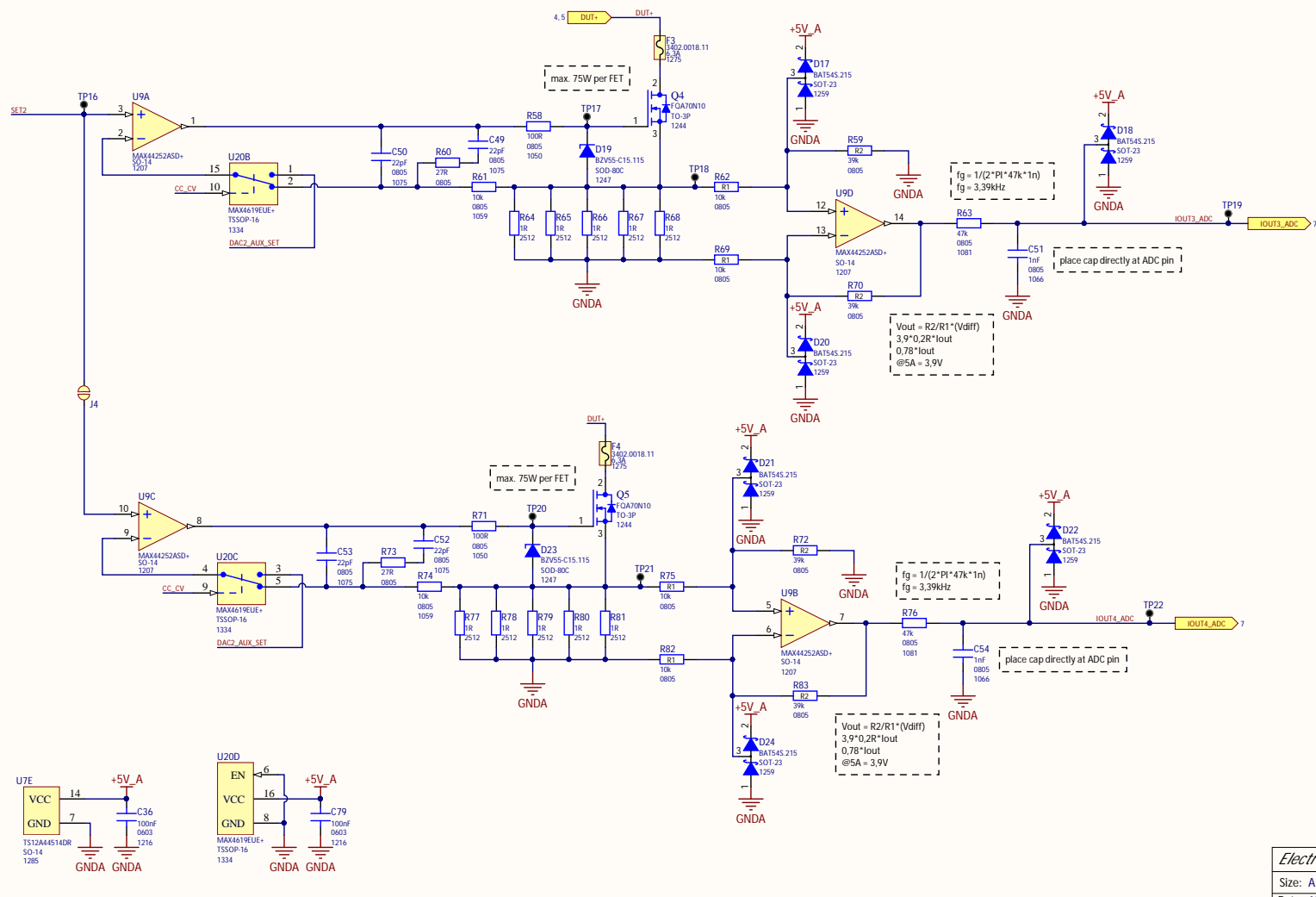


Current Control 3+4

A



B



D

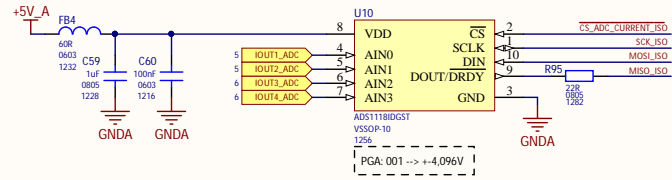
Electronic DC Load - Analog Part

Size: A3
Date: 12.02.2017 Time: 21:02:19 Sheet 6 of 7
Revision: V111

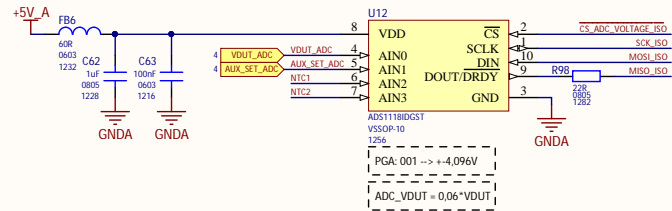


ADC - DAC - VREF - IO Expander - Temperature Sensor

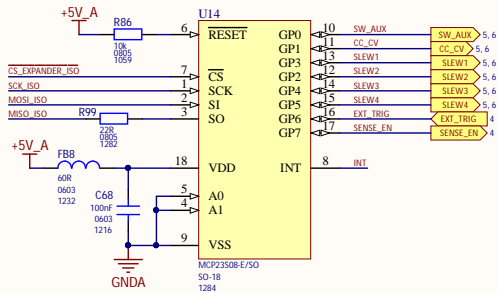
ADC 15 Bit - Single ended current measurement



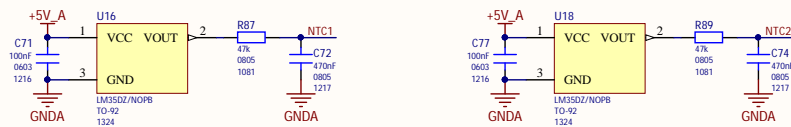
ADC 15 Bit - Input voltage/sense voltage, AUX voltage, NTC measurement



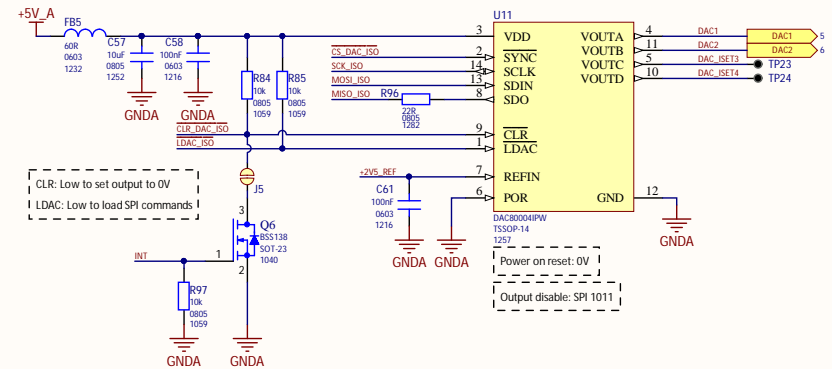
8-Bit SPI IO Expander



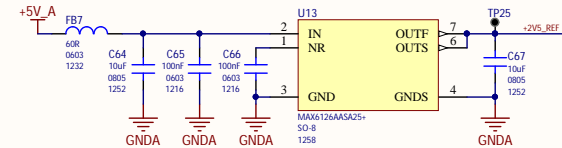
2x Temperature sensor at FETs



DAC 16 Bit - Current setting



VREF 2V5 0,02% 3ppm



Connector

