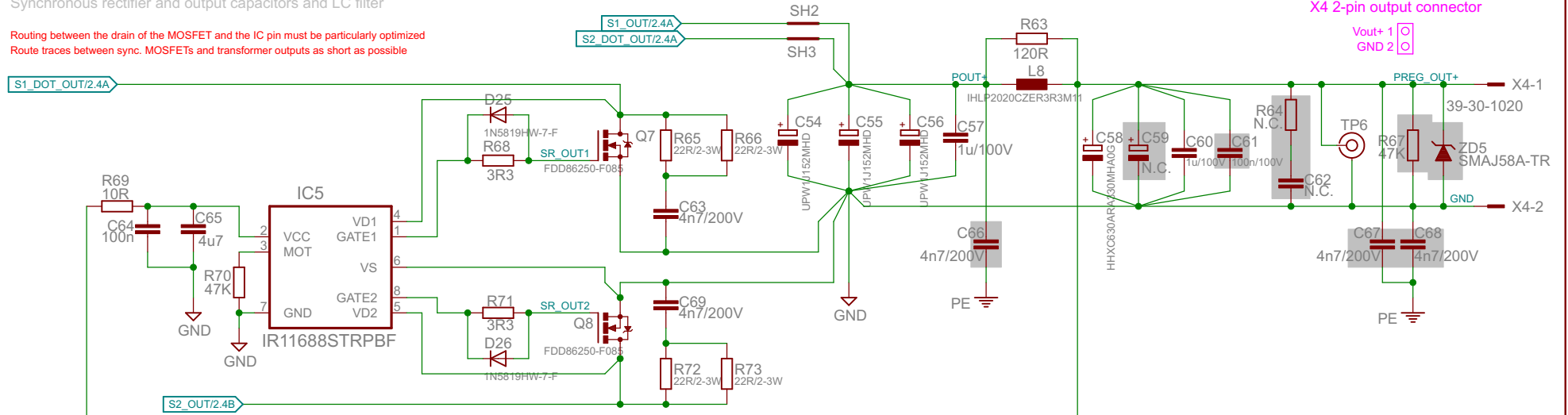
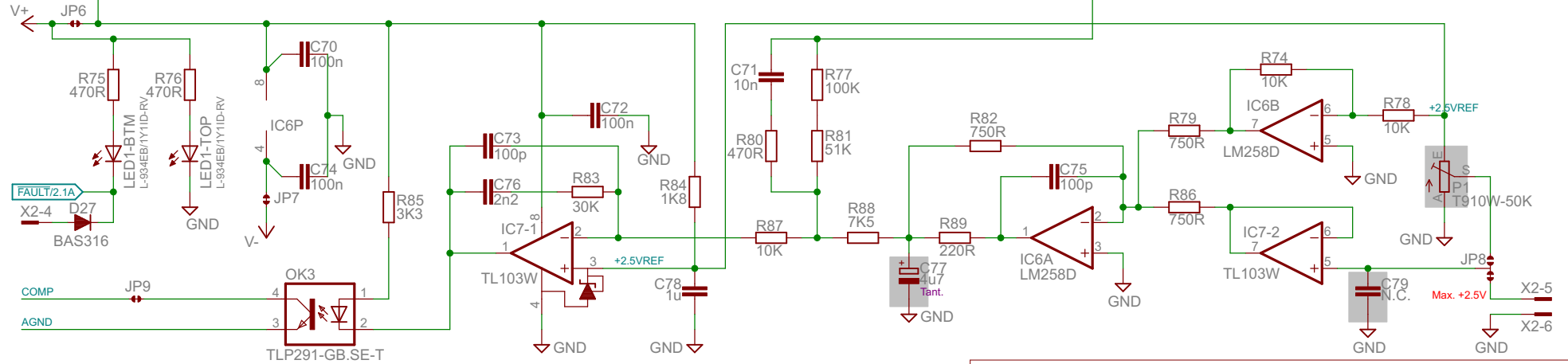


## Synchronous rectifier and output capacitors and LC filter

Routing between the drain of the MOSFET and the IC pin must be particularly optimized  
Route traces between sync. MOSFETs and transformer outputs as short as possible



## Voltage control loop + tracker



Place OK3 as close as possible to IC3

CF-DIC secondary side: SR driver  
NFB with Vout tracker

TITLE: EEZ CF-DIC r5B5

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