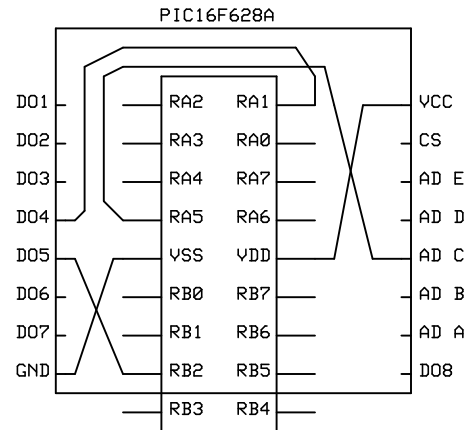


Pin Mapping

U9 - 7488	PIC16F628A
AD E	RA7
AD D	RA6
AD C	RA5
AD B	RB7
AD A	RB6
D01	RA2
D02	RA3
D03	RA4
D04	RA1
D05	RB2
D06	RB0
D07	RB1
D08	RB5



Inputs
RA<7:5>, RB<7:6>

Outputs
RA<4:0>, RB<5:0>

NOTES

1. Pin mapping list corrected in ver. 1.1

Company Name		
Schematic Name		
Designer's name	Rev 1.1	Page # or name
	12/6/18	