6 5 4 3 2 1

POWER SUPPLY/OSCILLATOR/COUNTER BOARD

POWER ON/OFF

RANGE 1 10 100 1K 10K 100K 1M 10M

FUNCTION SINE SQUARE TRIANGLE

OSCILLATOR SECTION

FREQUENCY/SWEEP SECTION

FREQUENCY FINE COURSE

SWEEP TIME WIDTH LIN/LOG ON/OFF

OUTPUT AMP BOARD

DUTY CYCLE LEVEL DC OFFSET DC OFFSET CMOS LEVEL

DUTY CYCLE

-20db

OUTPUT

TTL CMOS

OUTPUT

CAPACITIVE LOADS CONNECTION (MODEL 4011 ONLY)

POWER SUPPLY SECTION

COUNTER SECTION

4017 10MHz

4011 5MHz

COUNTER DISPLAY BOARD

GATE

KHz

Hz

UNUSED GATES

UNUSED DESIGNATORS

HIGHEST DESIGNATORS

POWER SUPPLY VOLTAGES

IC's	PIN #'s +5V	-5V	+15V	-15V	GND
U103,114,115, 118,122	14				
U105,300	8				4
U101	4	11			
U102	17	20			2,6,9, 10,11, 12,13
U104			4	11	10,3
U106			7	4	
U112	18				7,27
U113	16				2,8
U117	12				8,9
U118	2				8
U119	3				7
U120	10,16				7,8
U121	16				8
U123					7

NO.	DATE	REVISIONS	BY
G	1/30/96	ADD R333,R373 CHANGE VARIOUS VALUES	LCL
F	3/3/94	CHANGED RES. VALUES	LMD
E	3/6/86	ADD C344-346,&R356,8 T10	AMD
D	2/13/85	ADD RES. R304,306,307 ADD CAPS. .001,.1UF, VALS.R6,7,8,287,288,289 3.5K, 2.5K&.8K IC U122 FROM 73107 TO 73109	AMD
C	1/8/85	ADD U122,0106,R188,189, R90, C122	AMD
B	10/7/84	CHANGED SCHEMATIC FROM INK TO FILM BOARDS	AMD
A	10/28/84	PRELIMINARY FOR 1st PCB	AMD

MAXTEC INTERNATIONAL CORPORATION

6470 WEST RIVERLAND ST. BOISE, IDAHO

TITLE SCHEMATIC DRAWING — FUNCTION GENERATOR MODEL 4011/4017

UNLESS OTHERWISE SPECIFIED:
1. ALL DIMENSIONS ARE IN INCHES.
2. ALL ANGLES ARE ±1°.
4. ± DECIMAL .0020
3. ± .XX DECIMAL ±.010
4. ± .XXX DECIMAL ±.005

CONFIDENTIAL PROPRIETARY MATERIAL

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DRN. A. BUFF DATE 10/28/84
CHKD. A. POPOWSKI DATE 10/28/84
APP'D A. POPOWSKI DATE 10/28/84

SHEET 1 OF 1 SCALE 1:1 SIZE C

DRAWING NO. 300-040-9-005 G