

**Table 1–26: Troubleshooting Procedure For LED Display**

<b>Test Name</b>	<b>LED Display</b> (a failed test is preceded by a flashing decimal)	<b>A11 DRAM Processor/Display Troubleshooting Procedure</b>
Bus Control Read	1	Bus Control Register
Kernel RAM 1	2	Kernel RAM
Kernel RAM 2	3	Kernel RAM
Kernel RAM 3	4	Kernel RAM
BootROM Check Sum	5	BootROM Control
Bus Error Timeout	6	CPU Bus Error
Write Bus Control	7	Bus Control Register
CPU Interrupt Mask Register	8	CPU Interrupt
CPU Miscellaneous Register	9	CPU Interrupt
Timer Interrupt	a	Timer Interrupt
NV Ram Dsacks	b	Bdsack
FlashROM programming voltage is applied. NVRam is write protected.	c	On the A11 DRAM Processor/Display board press S1002 towards the back of the oscilloscope and cycle power.
FlashROM DSACKS	d	Bdsack
FlashROM Check Sum	e	FlashROM
ID Register The LED displays the A11 DRAM Processor/Display ID in hex: the most significant nibble (4 bits) first and then the least significant nibble.		ID Register