

The Columbia primitive output consists of a 7 segment LED (DS1) on the A11 DRAM Processor/Display board.

Table 1–28: A11 DRAM Processor/Display LED (DS1)

LED Display	Explanation
Decimal Point (DP)	When this precedes one of the hex numbers, the particular test the number represents has failed.
.E	This indicates an exception
.11	This indicates an interrupt
.P	This indicates a non maskable interrupt
.8	Displayed at power-up or reset
0	First displayed after power up
1	Bus Control Read diagnostic
2	Kernel RAM 1 diagnostic
3	Kernel RAM 2 diagnostic
4	Kernel RAM 3 diagnostic
5	BootROM Check Sum diagnostic
6	Bus Error Timeout diagnostic
7	Write Bus Control diagnostic
8	CPU Interrupt Mask Register diagnostic
9	CPU Miscellaneous Register diagnostic
a	Timer Interrupt diagnostic
b	NV RAM Dsacks diagnostic
c	FlashROM programming voltage is applied. The NV RAM is write protected.
d	FlashROM Dsacks
e	FlashROM Checksum